ABSTRACT

A receiver/transmitter circuit includes an antenna terminal, a transmitter circuit which outputs a transmission signal in response to a transmit control signal, a receiver circuit which receives a reception signal from the antenna terminal, and a waveform control circuit. The waveform control circuit outputs a switching signal having a gradual logic transition in response to the transmit control signal. The receiver/transmitter circuit further includes a first switch connected between the antenna terminal and the transmitter circuit, and a second switch connected between the antenna terminal and the receiver circuit. The first switch transfers the transmission signal to the antenna terminal in response to the switching signal.